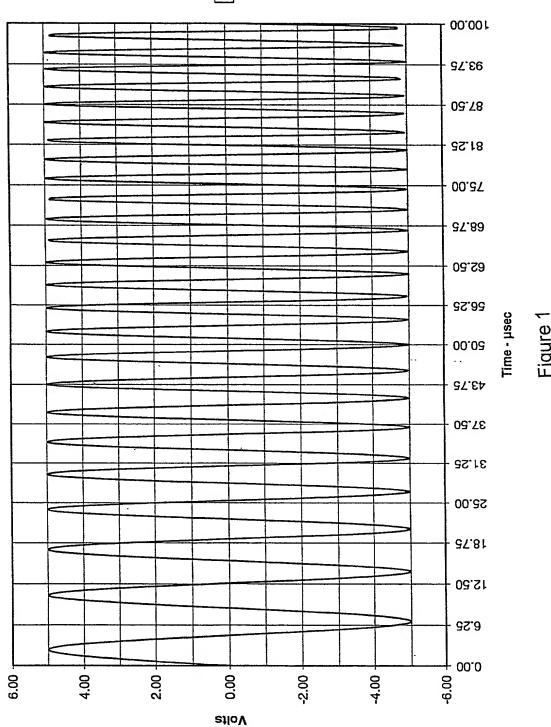
APPLN. FILING DATE: JANUARY 11, 2002
TITLE: METHODS AND APPARATUS FOR FEATURE RECOGNITION TIME SHIFT CORRELATION
INVENTOR(S): ROBERT CHAMBERLAIN
APPLICATION No.: UNASSIGNED
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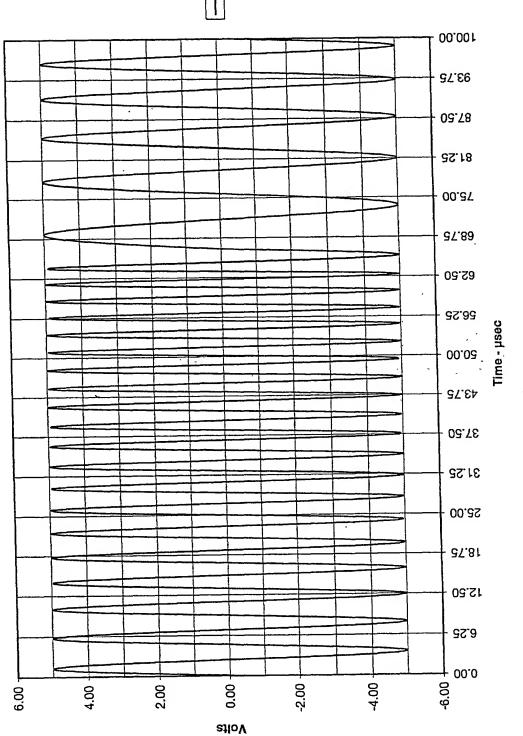




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INVENTOR(S): ROBERT CHAMBERLAIN
APPLICATION NO.: UNASSIGNED
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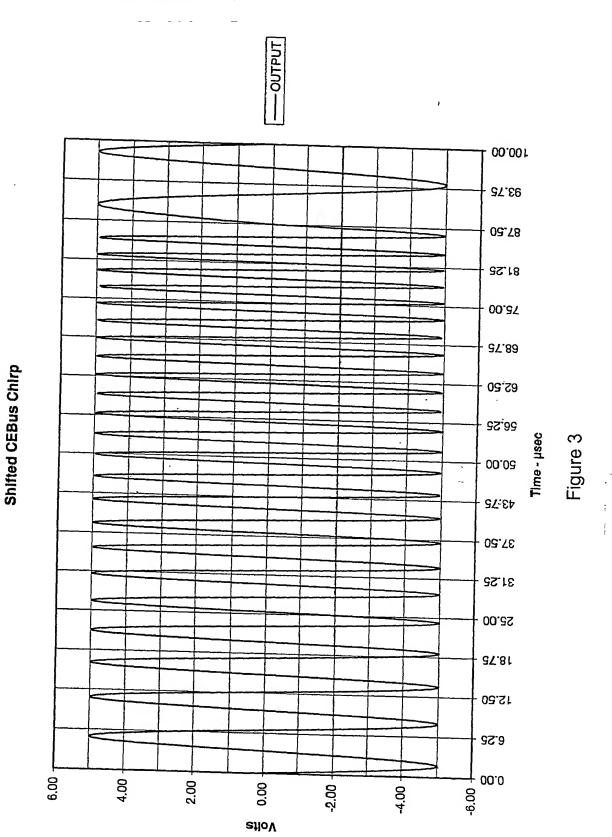




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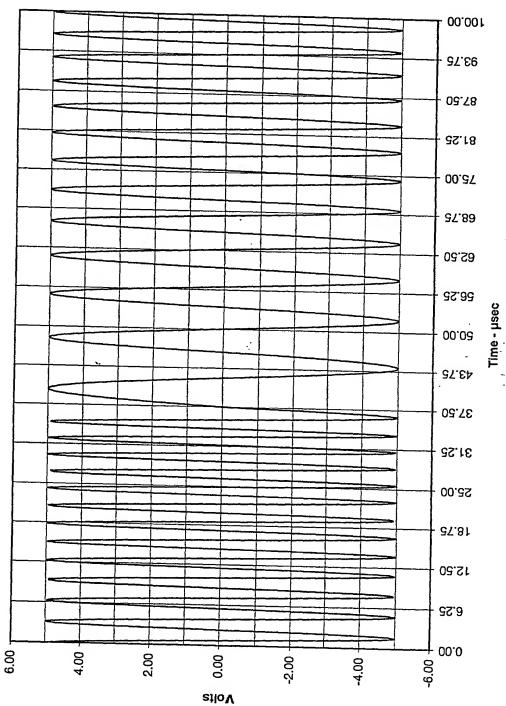
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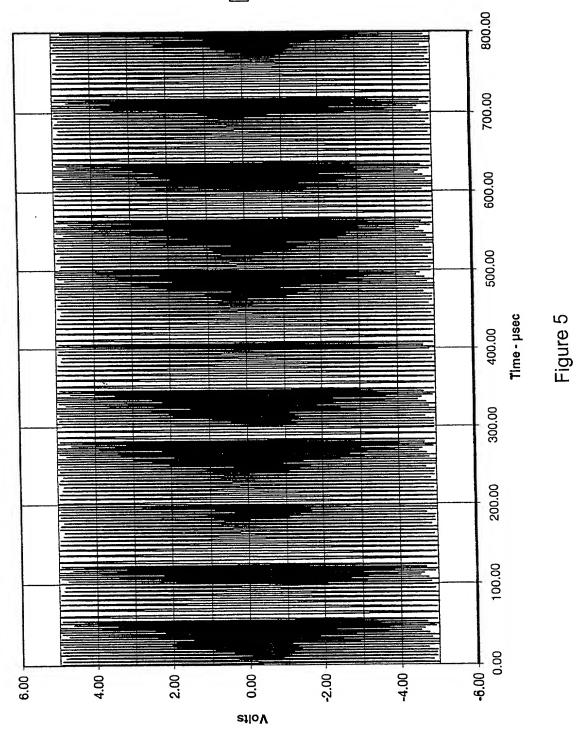


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INVENTOR(S): ROBERT CHAMBERLAIN
APPLICATION No.: UNASSIGNED
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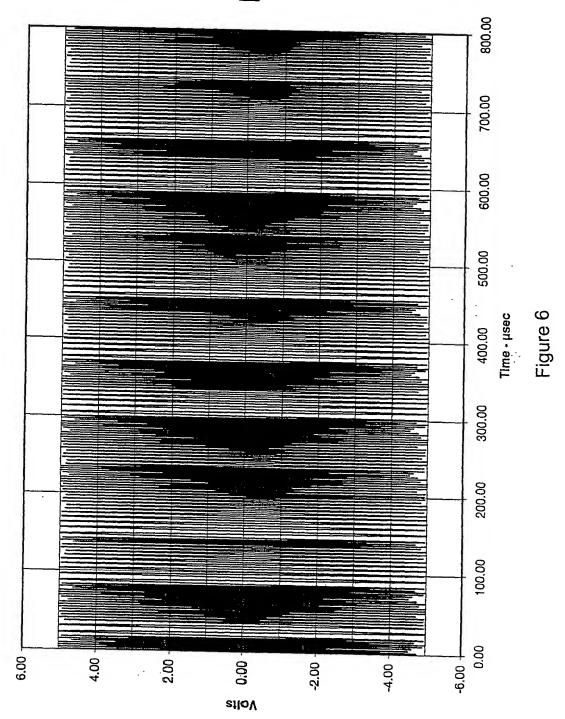


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APPLICATION NO.: UNASSIGNED
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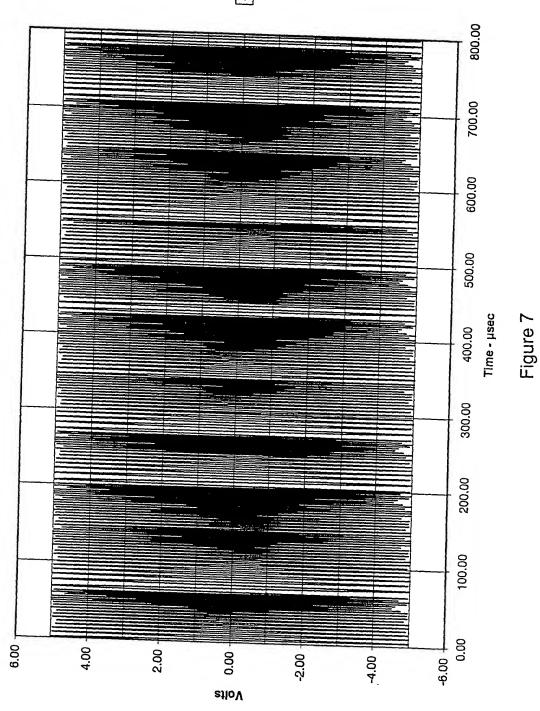


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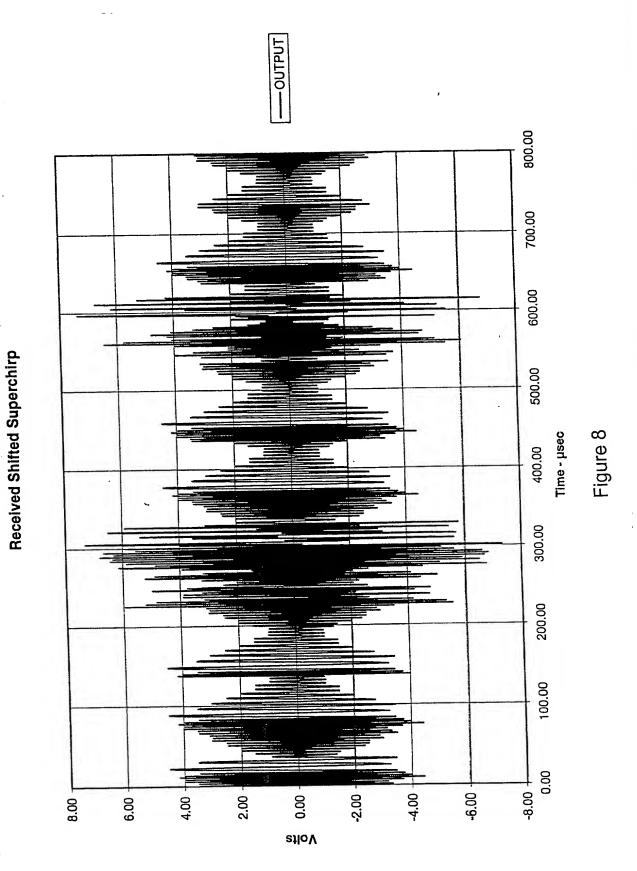
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TITLE: METHODS AND APPARATUS FOR FEATURE RECOGNITION TIME
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INVENTOR(S): ROBERT CHAMBERLAIN
APPLICATION NO.: UNASSIGNED
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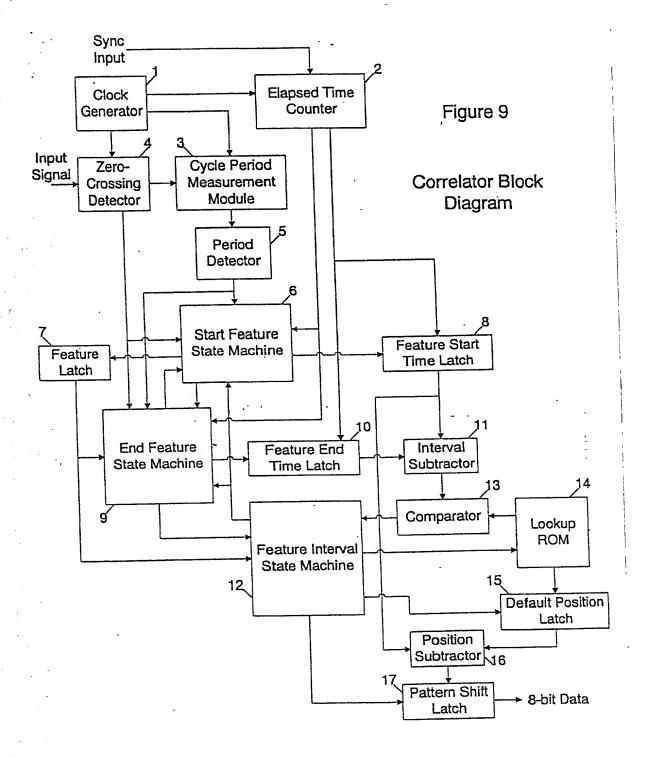


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APPLICATION NO.: UNASSIGNED

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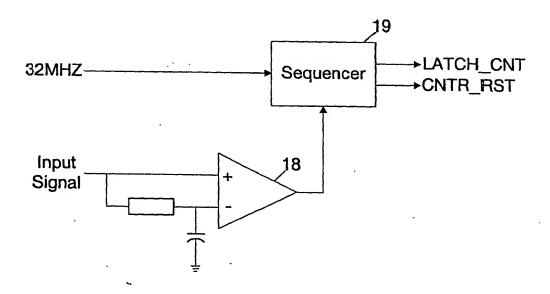


APPLN. FILING DATE: JANUARY 11, 2002
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Figure 10

# **Zero-Crossing Detector Block Diagram**



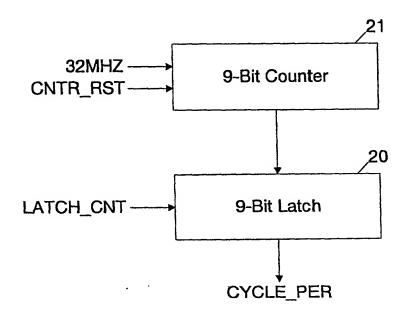
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INVENTOR(s): ROBERT CHAMBERLAIN APPLICATION No.: UNASSIGNED

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Figure 11

### Cycle Period Measurement Module **Block Diagram**

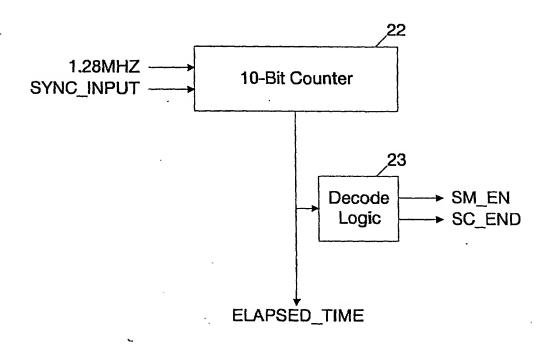


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Figure 12

### **Elapsed Time Counter Block Diagram**



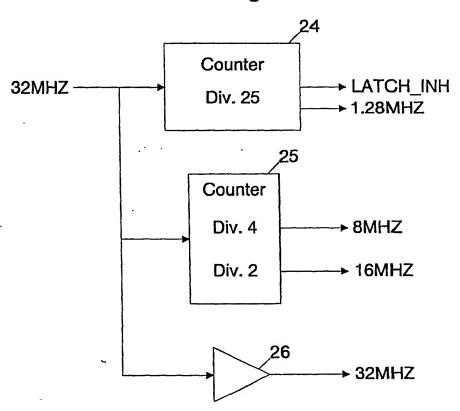
APPLN. FILING DATE: JANUARY 11, 2002
TITLE: METHODS AND APPARATUS FOR FEATURE RECOGNITION TIME
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APPLICATION NO .: UNASSIGNED

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Figure 13

## **Clock Generator Block Diagram**

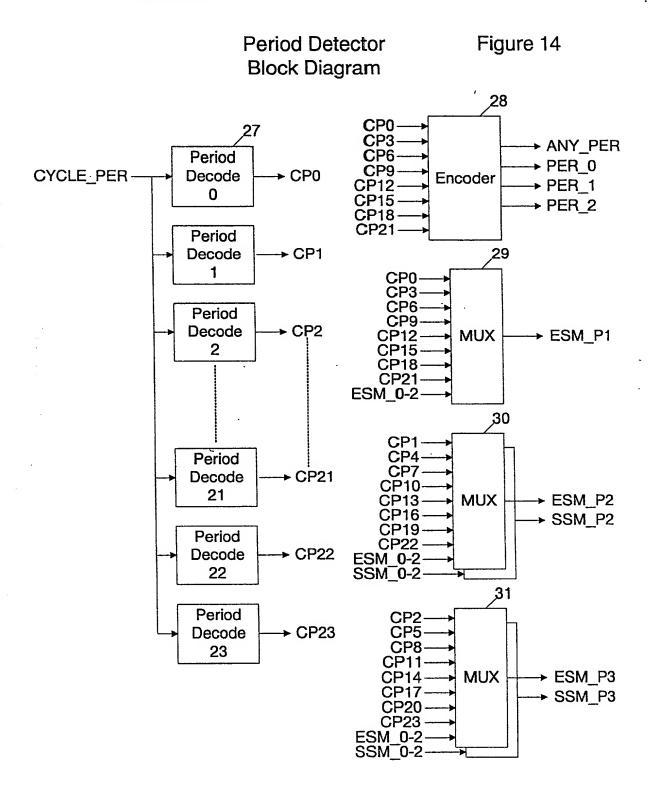


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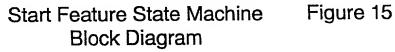
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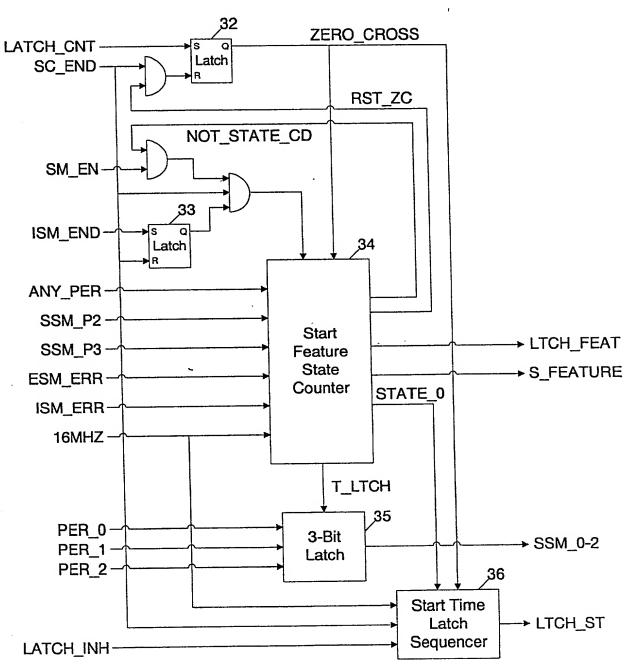
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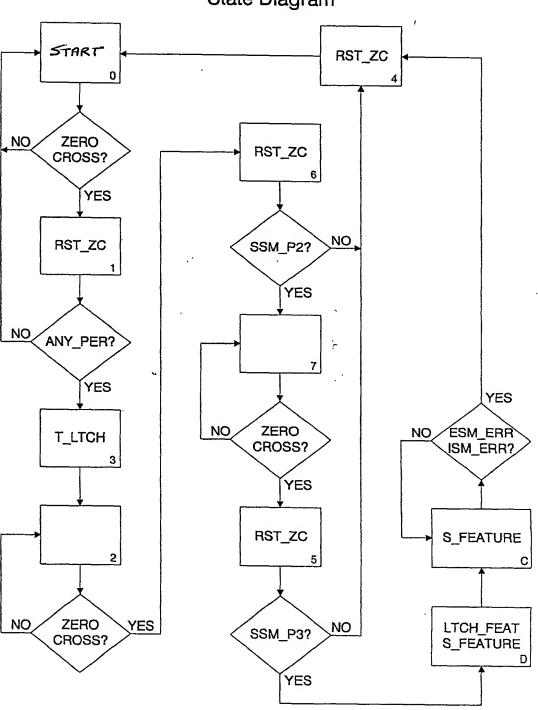
APPLN. FILING DATE: JANUARY 11. 2002
TITLE: METHODS AND APPARATUS FOR FEATURE RECOGNITION TIME SHIFT CORRELATION

INVENTOR(S): ROBERT CHAMBERLAIN
APPLICATION NO.: UNASSIGNED

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Figure 16





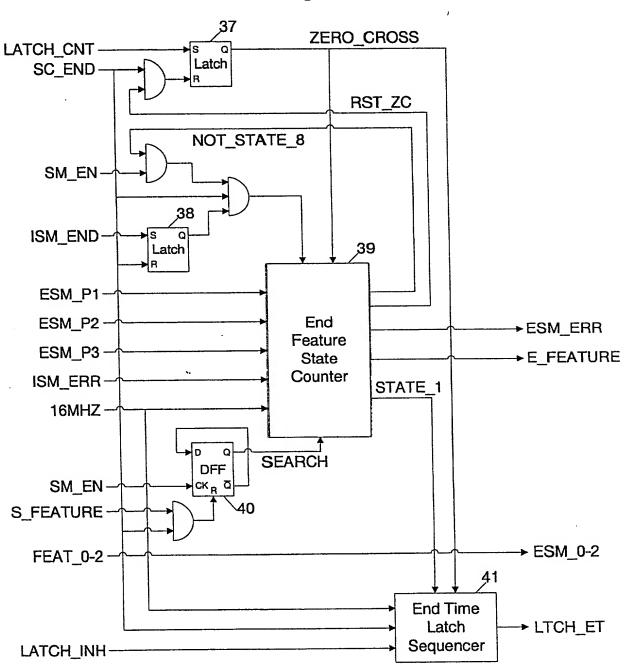
TITLE: METHODS AND APPARATUS FOR FEATURE RECOGNITION TIME SHIFT CORRELATION

INVENTOR(S): ROBERT CHAMBERLAIN

**APPLICATION NO.: UNASSIGNED** 

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Figure 17 **End Feature State Machine Block Diagram** 



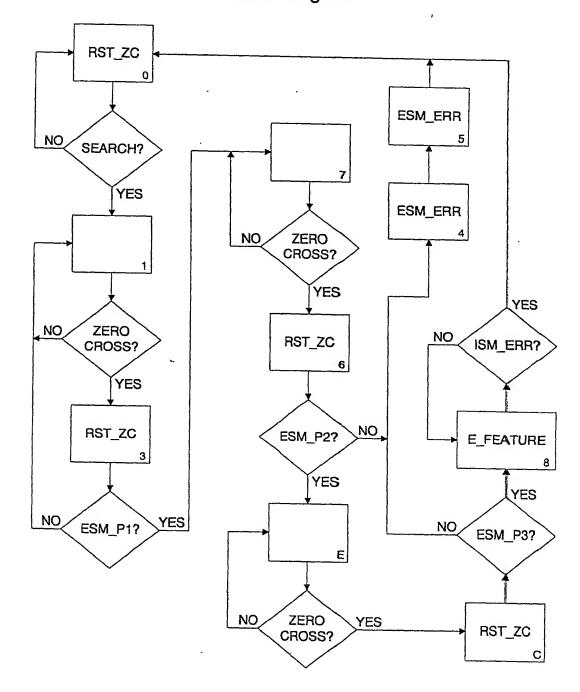
APPLN. FILING DATE: JANUARY 11, 2002
TITLE: METHODS AND APPARATUS FOR FEATURE RECOGNITION TIME

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APPLICATION NO.: UNASSIGNED **SHEET 18 OF 21** 

> **End Feature** State Machine State Diagram

Figure 18



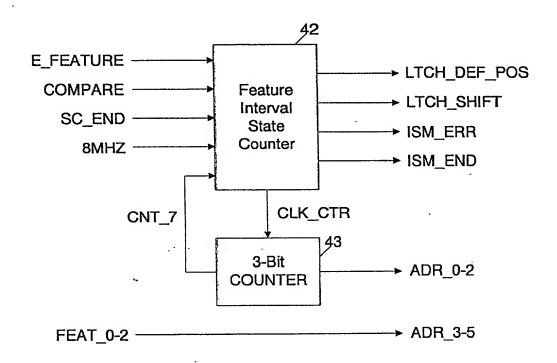
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#### Figure 19 Feature Interval State Machine **Block Diagram**



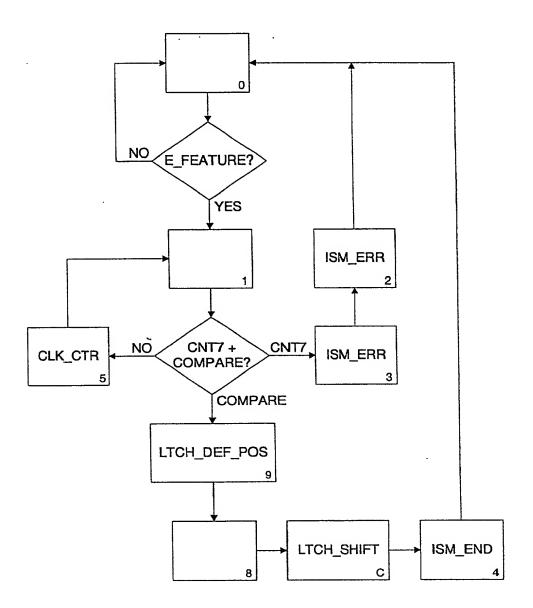
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Feature Interval State Machine State Diagram

Figure 20



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Figure 21

# Comparator **Block Diagram**

